SENSITIVITY ANALYSIS OF THE OUTPUT SIGNAL OF VLSI INVERTER-INTERCONNECT-INVERTER SYSTEM TO SELECTED PARAMETERS

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ABSTRACT: Interconnect parameters play an important role in signal propagation in VLSI systems. In the paper we present the sensitivity analysis to parameters of typical on-chip interconnects for ramp response. In the paper we show also the sensitivity to parameters for the threshold crossing time. We compute the output response of the interconnect and the threshold crossing time formulas based on the multiple scales method.

INTRODUCTION

The term sensitivity characterizes the influence of changing parameter value on system functionality. Sensitivity analysis permits to estimate, which of system parameters affect more and which one of them less on it. From the designer of VLSI circuits point of view this permits for identification of the number of variables, which have critical influence on operation of the circuit. For sensitivity analysis calculations numerical methods can be used. Changing value of various variables in the circuit one determines importance and influence of each variable of the circuit \[1\]. The problem of sensitivity analysis of single or coupled interconnects has been considered in paper \[2\]. The authors have proposed effective method of optimization permitting for selection of the trace width in a way, which minimize the delay of the signal. The method is based on calculation of sensitivity of interconnect delay using the method of moments. In \[3\] authors use the sensitivity of the system for optimization of parameters. Sensitivity analysis is carry out in frequency domain. One of the basic works concerning sensitivity analysis is \[4\]. The authors introduce equations describing sensitivity from transmission line equations and build up the model for sensitivity simulation in time domain.

Faster circuits, higher frequencies, smaller die sizes in VLSI modern technology causes new problems in system designs. During that process there is a need to appropriate model assumption to ensure that the simulation of the system will give good accuracy. Both the gates and interconnect must be considered during system modelling. Due to the resistance of the modern interconnects are smaller than the lossless line impedance \(Z_0\) (1) there is no longer possible to model the interconnect by RC transmission line, and the RLC transmission line must be considered \[5,6\]

\[
\frac{R}{2Z_0} \leq 1. \tag{1}
\]

Although the high growth of device speeds the on-chip interconnect has not been scaling so fast. Especially global and clock wires could be 2-8x the minimum dimensions \[5\]. The higher level
clock interconnects might have total resistance $8.2-21.5\,\Omega$ for 1 mm line length. Most of the application for simulation use only RC model, and that approach does not give good accuracy [5]. Typical simulation problems consider simulation of the one interconnect or more coupled interconnects.

In previous work [7] we derive the method for calculation the step and ramp responses for clock and global wires assuming RLC transmission model and the global resistance less than lossless interconnect impedance. That assumption allows treat interconnect as low-loss transmission line with high inductance influence. For the system of transmission line differential equations with initial and boundary conditions we could use the perturbation method of multiple scales, using the $\varepsilon = \frac{R_i}{Z_0}$ as perturbation parameter ($R_i = R \cdot d$).

In our work we present sensitivity analysis based on the previously derived, by the method of multiple scales, relationships for the voltage at output of interconnect in the system inverter – interconnect – inverter. Simulations used in our investigation of the interconnect response, both the output response and threshold crossing time calculation can be highly sensitive to small changes in the parameter values.

The paper is organized as follows: in the second section there we shortly present the method of step and ramp response calculation and we show how to obtain the threshold crossing time from the output response. In the next section we present the sensitivity analysis of the output response and threshold crossing time for typical on-chips interconnects to interconnect parameters. We conclude in the last section.

**MODEL OF INVERTER-INTERCONNECT-INVERTER SYSTEM**

In this section we shortly present the method of computation the step and ramp responses for clock and global wires assuming RLC transmission model (Fig. 1) and the global resistance less than lossless interconnect impedance. That assumption allows treat the line as low-loss line with high inductance influence.

![Fig. 1 The system inverter-interconnect-inverter.](image)

For the system of differential equation (2) with initial and boundary conditions (3,4) we could use the perturbation method of multiple scales, using the $\varepsilon = \frac{R_i}{Z_0}$ as perturbation parameter ($R_i = R \cdot d$).
\[
\begin{aligned}
- \frac{\partial v}{\partial x} &= R_i + L \frac{\partial i}{\partial t}, \\
- \frac{\partial i}{\partial x} &= C \frac{\partial v}{\partial t}
\end{aligned}
\]

\(2)\)

\(i(x,0) = 0, \quad v(x,0) = 0, \quad e(t) - R_i i(0,t) = v(0,t), \quad -i(d,t) = C_0 \frac{\partial v(d,t)}{\partial t}\)

\(3)\)

\(4)\)

where

\(R, L, C – \) line parameters, \(C_0 – \) input inverter capacitance, \(R_s – \) output inverter resistance, \(i(x,t), v(x,t) – \) current and voltage in line, respectively, \(d – \) line length, \(t, x – \) time and space variable, respectively.

After scaling system (2) to obtain the perturbation parameter in the system, we can observe, that resistance of the interconnect influence only for perturbation parameter value. After scaling we have:

\[
y = \frac{x}{d}, \quad \tau = \frac{t}{\sqrt{L_i C_i}}, \quad \tilde{v} = -\frac{C_i}{L_i} v,
\]

\(5)\)

\[
\tilde{e} = \frac{C_i e_i}{L_i}, \quad \beta = \frac{C_i R_i}{L_i}
\]

Output response calculation

The system (2-4) after scaling (5) can be rewritten as:

\[
\begin{aligned}
\frac{\partial \tilde{v}}{\partial y} &= \tilde{i} + \frac{\partial \tilde{i}}{\partial \tau}, \\
\frac{\partial \tilde{i}}{\partial y} &= \frac{\partial \tilde{v}}{\partial \tau}, \\
\tilde{e}(\tau) - \beta \tilde{i}(0, \tau) &= -\tilde{v}(0, \tau), \\
-\tilde{i}(1, \tau) &= \frac{C_0}{C_i} \frac{\partial \tilde{v}(1, \tau)}{\partial \tau}
\end{aligned}
\]

\(6)\)

\(7)\)

The multiple scales method [6] requires expansion of the solution for differential equations into a power series of perturbation parameter, which values are relatively small. Additionally, new space variables are introduced. In our analysis we assume the line resistance in low resistance interconnects is small compared to the lossless line impedance \(Z_0\) (\(Z_0 > R_t\)), the perturbation parameter is \(\varepsilon\), and we limit the expansion to two terms and two space variables. For the system of equations (6) we have

\[
\begin{aligned}
\tilde{v}(y, \tau) &= \tilde{v}_0(y_0, y_1, \tau) + \varepsilon \cdot \tilde{v}_1(y_0, y_1, \tau), \\
i(y, \tau) &= i_0(y_0, y_1, \tau) + \varepsilon \cdot i_1(y_0, y_1, \tau), \\
y_0 &= y, \quad y_1 = \varepsilon \cdot y.
\end{aligned}
\]
Than we obtain new systems of differential equation, the result of alternation equation depend on previous equation. The method allows to compute the lossless line output step response comply the losses parameter $\epsilon$. The way of calculating the step output response for such a system is presented in [7]. For the first traveling wave we obtain:

$$v_1(d,t) = v_{01}(d,t) + \epsilon v_{11}(d,t) =$$

$$= \frac{E_0}{\beta + 1} \left( A_2 \frac{t-T}{T} e^{-\frac{\alpha}{T}(t-T)} - B_2 \left( 1 - e^{-\frac{\alpha}{T}(t-T)} \right) + C_2 \frac{t-T}{T} \right) \cdot 1(t-T),$$

for time $0 < t < 3T$

where:

$$A_2 = \epsilon \cdot e^{-0.5\epsilon}, \quad C_2 = \epsilon \cdot \frac{\beta}{\beta + 1}, \quad B_2 = \left( \frac{\epsilon}{\alpha} - 2 \right) e^{-0.5\epsilon} + \frac{\beta}{\beta + 1} \frac{\epsilon}{\alpha}.$$

The simulations show that the approximation gives good results, especially during the rise time of output signal. When the aim is to calculate the threshold crossing time or to predict the behavior of the interconnect in that range of time, the accuracy of formula (8) is enough.

The fast circuits generate the rising time of the input signal comparable to the time delay, therefore in many on-chip interconnects we have to take into account the influence of the input voltage rise time $T_r$. We model such a problem with the ramp excitation:

$$v_{in}(t) = \frac{E_0}{T_r} t \cdot 1(t) - \frac{E_0}{T_r} (t - T_r) \cdot 1(t - T_r) \quad (9)$$

We must consider two cases – one for $0 < t < T_r$, and the other for $t > T_r$. Denoting the response to ramp excitation at the end of the interconnect as $v_{out}$, and we have the final output voltage signal [7]:

$$v_r(t) = \frac{E_0}{(\beta + 1)T_r} \left[ \begin{array}{c} C_2 \frac{(t-T)^2}{2} - B_2 \frac{t-T}{T} + \left( K_1 \frac{t-T}{T} - K_2 \right) e^{-\frac{\alpha}{T}(t-T)} + K_2 \end{array} \right], \quad dla \quad t - T \leq T_r$$

$$v_r(t) = \frac{E_0}{(\beta + 1)T_r} \left[ \begin{array}{c} K_1 \frac{t-T}{T} e^{-\frac{\alpha}{T}(t-T)} T_2 + C_2 \cdot T_r \frac{t-T}{T} - \left( K_2 \cdot T_2 - K_1 \cdot T_r e^{\alpha T_r} \right) e^{-\frac{\alpha}{T}(t-T)} \end{array} \right],$$

$$dla \quad t - T \geq T_r,$$

$$K_1 = -\frac{A_2}{\alpha}, \quad K_2 = \left( \frac{B_2 - K_1}{\alpha} \right), \quad T_2 = 1 - e^{\alpha T_r}.$$

The presented approach allows calculating the approximated ramp excitation response. The exemplary ramp response is presented on Fig. 2.
The computation of threshold crossing time is a challenge many authors try to deal with, e.g. [6]. For the ramp response (10) we can calculate the threshold crossing time, during the ramp times (t<Tr) solving (11) with respect to t:

$$\rho = \frac{K_r T_r e^{\alpha z} + C_2 T_r e^{-\alpha z} e^{\alpha T_r} - C_2 T_r^2 e^{\alpha T_r} - B_2 T_r}{(\beta + 1)T_r}.$$  \hspace{1cm} (11)$$

where $\rho$ is the threshold value (0$\div$1).

Equation (11) can be reduced to the form

$$T_3 = \tilde{e} \cdot e^{-\alpha \cdot \tilde{z}} + \frac{C_2 \tilde{e}}{K_1 T_2} e^{-\alpha \cdot \tilde{z}} \cdot T_4.$$  \hspace{1cm} (12)$$

and then as:

$$a_{r2} - K_r \cdot z = z e^{\tilde{z}}.$$  \hspace{1cm} (13)$$

where $a_{r2}$ and $K_r$ are constants, and $z$ is the time function.

We can calculate the threshold crossing time using the iteration method of solving nonlinear equation $x=f(x)$, as:

$$z^{(n+1)} = W(a_{r2} - K_r z^{(n)}).$$  \hspace{1cm} (14)$$

Fig. 2 Ramp response for the interconnect (Rt=25$\Omega$, Lt=5nH, Ct=1pF, C0=0.1pF, Rw=25$\Omega$, Tr=30ps) the output response calculated with (10) - b, compared with PSPICE simulation - a.
Then the threshold crossing time will take the form:

\[
T_P^{(n)} = \left( -\frac{1}{\alpha} z^{(n)} - T_4 \right) T,
\]

(15)

where \( z^{(n)} \) comes from (14), and for \( z^{(0)} \) we take 0, what gives \( z^{(1)} = W(a_{r2}) \), where \( W(x) \) is Lambert W function.

**SENSITIVITY ANALYSIS**

In our work we present sensitivity analysis based on the above derived, by the method of multiple scales, relationships for the voltage at output of interconnect in the system inverter – interconnect – inverter. Simulations used in investigation of the interconnect response, both the output response and threshold crossing time calculation can be highly sensitive to small changes in the parameter values. Due to generation by the presented approach the closed form formulas for output response (10), we could calculate the sensitivity of the output response with the formula:

\[
S_\nu^\lambda = \frac{\lambda}{\nu} \frac{\partial \nu}{\partial \lambda}
\]

(16a)

and for the threshold crossing time:

\[
S_\nu^\rho = \frac{\lambda}{\rho} \frac{\partial \rho}{\partial \lambda}
\]

(16b)

where \( \lambda \) is a parameter with respect to which sensitivity is calculated.

In order to compare the results obtained analytically by means of the formulas (16) with sensitivity obtained in PSPICE we introduce the following formula using the small perturbation of parameter \( \Delta \lambda \):

\[
S_\nu^\Delta = \left( \frac{v(t, \lambda) - v(t, \lambda + \Delta \lambda)}{v(t, \lambda) \cdot \Delta \lambda} \right) \cdot \lambda + \text{hot} \approx \frac{v(t, \lambda) - v(t, \lambda \cdot 1.01)}{v(t, \lambda)} 100 = S_{\nu\text{SPICE}}
\]

(17)

where “hot” – means higher order terms, which has been neglected in further considerations. The small perturbation of parameter \( \Delta \lambda \) is chosen for simulation as equal to 1% of \( \lambda \). Simulation conducted for each of parameters confirm, that for all calculations (except for \( L \) and \( C \), which change affect the time delay what will be analyzed later) value 1% is appropriate to obtain the proper results for sensitivity calculations. Symbol \( v \) is the voltage at the end of interconnect and is denoted as \( v_s \) for step response and \( v_r \) for ramp response. In the paper sensitivity of the voltage with respect to set of parameters determining general features of circuit, such as perturbation parameter \( \varepsilon \) responsible for losses, parameters \( \alpha, \beta \) depending on supply and load parameters of circuit and parameters RLC of the transmission line model will be presented. Applying definition formula (16) and calculating relevant derivatives (with respect to relevant parameters) one can obtain formula in time domain for sensitivity, which may be used to calculate sensitivity in fixed time point as a function of various parameters or for fixed parameters as a function of time.
Sensitivity analysis of the step response with respect to losses and input/output parameters

First of all we calculate sensitivity of the step response with respect to perturbation parameter $\varepsilon$ and next we investigate influence of it on results obtained by the multiple scale method. The change of this parameter, with the other parameters unchanged, denotes the change of resistance of interconnect $R$ on the same percentage value. Sensitivity of the step response with respect to $\varepsilon$ is:

$$ S_{v_r}^{\varepsilon} = \frac{\varepsilon}{v_r} \frac{\partial v_r}{\partial \varepsilon} = \varepsilon \frac{\partial}{\partial \varepsilon} \left[ \frac{E_0}{(\beta + 1)T_r} \left[ \frac{C_2}{2} \tilde{t}^2 - B_2 \tilde{t} + (K_1 \tilde{t} - K_2) e^{-\alpha t} + K_3 \right] \right] $$

$$ dla \quad \tilde{t} \leq T_r / T $$

$$ S_{v_r}^{\varepsilon} = \frac{E_0}{v_r (\beta + 1)T_r} \left[ K_1 \tilde{t} e^{-\alpha t} T_2 + C_2 \cdot T_r \tilde{t} - (K_2 \cdot T_2 - K_1 \cdot T_r e^{\alpha t}) e^{-\alpha t} - \frac{C_3 T_r^2}{2} \right] $$

$$ dla \quad \tilde{t} > T_r / T $$

where $\tilde{t} = (t - T) / T$. Finally, after calculation we obtain:

$$ S_{v_r}^{\varepsilon} = \varepsilon \frac{E_0}{v_r (\beta + 1)T_r} \left[ \frac{C_2}{2} \tilde{t}^2 - B_2 \tilde{t} + (K_1 \tilde{t} - K_2) e^{-\alpha t} + K_3 \right] $$

$$ dla \quad \tilde{t} \leq T_r / T $$

or

$$ S_{v_r}^{\varepsilon} = \frac{E_0}{v_r (\beta + 1)T_r} \left[ K_1 \tilde{t} e^{-\alpha t} T_2 + C_2 \cdot T_r \tilde{t} - (K_2 \cdot T_2 - K_1 \cdot T_r e^{\alpha t}) e^{-\alpha t} - \frac{C_3 T_r^2}{2} - B_2 \tilde{t} \right] $$

$$ dla \quad \tilde{t} > T_r / T $$

The sensitivity to parameter $\varepsilon$ is the same as sensitivity to the line resistance $R$. In Fig. 3 we can see comparison of results obtained by formula (19) and during PSpice simulation (using formula (17)).
Fig. 3 Comparison of sensitivity of ramp response with respect to $R$ calculated by formula (17) and (16), for exemplary parameters of highly inductive interconnects ($R_t=25\Omega$, $L_t=5nH$, $C_t=1pF$, $C_0=0.1pF$ and $R_w=25\Omega$, $\varepsilon=0.354$, $\alpha=10$, $\beta=0.354$), a – sensitivity calculated in PSPICE, b – sensitivity calculated by means of (19).

Using relationship (19) we can show dependence of sensitivity with respect to parameter $R$ for selected sets of parameters (Fig. 4).

Fig. 4 Sensitivity of step response with respect to $\varepsilon$, as a function of time, for exemplary parameters of highly inductive interconnects ($R_t=25\Omega$, $L_t=5nH$, $C_t=1pF$, $C_0=0.5pF$ and $R_w=25\Omega$, $\varepsilon=0.354$, $\alpha=2$, $\beta=0.354$) and for various values of parameter $\varepsilon$, equivalent to various values of resistance of interconnect. a – for $\varepsilon=0.177$ $\Rightarrow R=12.5\Omega$, b – for $\varepsilon=0.354 R=25\Omega$, c – for $\varepsilon=0.631 \Rightarrow R=37.5\Omega$, d – for $\varepsilon=0.708 \Rightarrow R=50\Omega$.

For early times sensitivity with respect to $\varepsilon$ increase and for times close to $3T$ however decrease. In principle on sensitivity values have influence all parameters of the interconnect. The greatest sensitivities are caused by large values of $R$ (Fig. 4), load capacitance (small $\alpha$) and small values of input resistance. For considered sets of parameters the worst case value of the sensitivity $0.8$ was obtained for $\varepsilon=1$, $\alpha=1$, $\beta=0.1$.

The next parameter with respect to which we investigate sensitivity of the ramp response is $\alpha$ containing input capacitance of the load inverter. The change of this parameter, with remaining
parameters kept constant, denotes relevant change of inverter capacitance \( C_0 \). The dependency is \( \alpha = C/C_0 \). We have:

\[
\frac{dv_r}{dC_0} = \frac{\partial v_r}{\partial C_0} + \frac{\partial v_r}{\partial \alpha} \frac{\partial \alpha}{\partial C_0} + \frac{\partial v_r}{\partial \beta} \frac{\partial \beta}{\partial C_0} + \frac{\partial v_r}{\partial \epsilon} \frac{\partial \epsilon}{\partial C_0} + \frac{\partial v_r}{\partial t} \frac{\partial t}{\partial C_0}.
\]

(20)

Since:

\[
\frac{\partial \alpha}{\partial C_0} = -\frac{C}{C_0^2}, \quad \frac{\partial \beta}{\partial C_0} = 0, \quad \frac{\partial \epsilon}{\partial C_0} = 0,
\]

derivative of the ramp response has form:

\[
v_r' C_0 = \frac{dv_r}{dC_0} = -\frac{C}{C_0^2} \frac{\partial v_r}{\partial \alpha} = \frac{C}{C_0^2} v_r' \alpha
\]

and sensitivity:

\[
S_{v_r}^{C_0} = -\frac{C}{C_0} \frac{v_r' \alpha}{v_r} = -\frac{\alpha v_r' \alpha}{v_r} = S_{v_r}^{\alpha}
\]

(21)

Similarly to the case of perturbation parameter \( \epsilon \), we can calculate ramp response sensitivity with respect to \( \alpha \) parameter from the formula:

\[
S_{v_r}^{\alpha} = \frac{\alpha}{v_r} \frac{E_0}{(\beta + 1)T_r} \left[ \frac{C_1}{2} \tilde{T}^2 - B_2 \tilde{T} + \left( K_{2a} \tilde{T} - K_{2a} \right) e^{-\tilde{T}} - \tilde{T} \left( K_1 \tilde{T} - K_2 \right) e^{-\tilde{T}} + K_{2a} \right] \quad \text{dla } \tilde{T} \leq T_r / T
\]

or

\[
S_{v_r}^{\alpha} = \frac{\alpha}{v_r} \frac{E_0}{(\beta + 1)T_r} \left[ K_{2a} \tilde{T} e^{-\tilde{T}} - \tilde{T} K_1 T e^{-\tilde{T}} + C_{2a} T \tilde{T} + \left( K_{2a} \tilde{T} + C_{2a} T \tilde{T} - K_{2a} T e^{-\tilde{T}} - T K_1 T e^{-\tilde{T}} C_{2a} T \right) e^{-\tilde{T}} + \tilde{T} \left( K_2 T_2 - K T e^{-\tilde{T}} \right) e^{-\tilde{T}} - C_{2a} T \tilde{T}^2 - B_2 \tilde{T} \right] \quad \text{dla } \tilde{T} > T_r / T
\]

(22)

where \( A_{2a}, B_{2a}, C_{2a} \) are derivatives of \( A_2, B_2 \) and \( C_2 \) with respect to \( \alpha \).

Comparison of results from PSpice and from (22) confirms usefulness of the applied method of sensitivity calculation and the errors for the case of sensitivity to \( \alpha \) are less than for the case of sensitivity to \( \epsilon \).
Fig. 5 Comparison of sensitivity of ramp response with respect to $\alpha$, as a function of time, from PSpice and from (22), for exemplary parameters of highly inductive interconnects ($R_t=25\Omega$, $L_t=5nH$, $C_t=1pF$, $C_0=1pF$ and $R_w=25\Omega$, $\varepsilon=0.354$, $\alpha=10$, $\delta=0.354$), a – sensitivity calculated in PSpice, b – sensitivity calculated from (22).

Dependency of sensitivity to parameter $\alpha$ with respect to influence other parameters is shown in (Fig. 6). Similarly as previously the plots are presented against normalized time $\tilde{t}$.

Fig. 6 Comparison of sensitivity of ramp response with respect to $\alpha$, as a function of time, for exemplary parameters of highly inductive interconnects ($R_t=25\Omega$, $L_t=5nH$, $C_t=1pF$, $C_0=0.5pF$ and $R_w=25\Omega$, $\varepsilon=0.354$, $\alpha=2$, $\delta=0.354$) for various values of parameter $\alpha$, equivalent to load capacitance changes of interconnect. a – sensitivity for parameter $\alpha=1 \Rightarrow C_0=1pF$, b – for $\alpha=2 \Rightarrow C_0=0.5pF$, c – for $\alpha=4 \Rightarrow C_0=0.25pF$, d – for $\alpha=10 \Rightarrow C_0=0.1pF$.

Sensitivity to parameter $\alpha$, in very small degree depends on changes of source resistance however strongly depends on changes of load capacitance. Sensitivity value at the moment of signal appearance at the output of interconnect is always the same and equal to one, however at the end of first wave duration is resoluteness lower (close to zero) for interconnects loaded by very small capacitances. Dependence from interconnect resistance changes (Fig. 6) is resoluteness lower than in the case of changes of the load, yet one can see tendency, that increase of resistance of interconnect ($\varepsilon$ is growing) entail increase of sensitivity.
Now we consider the sensitivity of the ramp response to parameter $\beta$ connected with the sending inverter. This parameter is proportional to the output resistance of sending inverter and can be calculated from the formula:

$$
S^\beta_{v_t} = \frac{\beta E_0}{v_r T_r} \left[ \frac{C_{2\beta} \tilde{t}^2 - B_{2\beta} \tilde{t} + (K_{1\beta} \tilde{t} - K_{2\beta}) e^{-\alpha \tilde{t}} + K'_{2\beta}}{(\beta + 1)} \right] +
$$

$$
- \left[ \frac{C_{1\beta} \tilde{t}^2 - B_{1\beta} \tilde{t} + (K_{\beta} \tilde{t} - K_{2}) e^{-\alpha \tilde{t}} + K_2}{(\beta + 1)^2} \right]
$$

dla $\tilde{t} \leq T_r / T$

$$
S^\beta = \frac{\beta E_0}{v_r T_r} \left[ \frac{K_{1\beta} e^{-\alpha \tilde{t}} T_2 + C_{2\beta} T_r \tilde{t} - (K_{2\beta} T_2 - K_{1\beta} T_r e^{\alpha T_r}) e^{-\alpha \tilde{t}} - \frac{C_{2\beta} T^2_r}{2} - B_{2\beta} T_r}{(\beta + 1)} \right] +
$$

$$
- \left[ \frac{K_{\beta} e^{-\alpha \tilde{t}} T_2 + C_{2\beta} T_r \tilde{t} - (K_{2} T_2 - K_{\beta} T_r e^{\alpha T_r}) e^{-\alpha \tilde{t}} - \frac{C_{2} T^2_r}{2} - B_{2} T_r}{(\beta + 1)^2} \right]
$$

dla $\tilde{t} > T_r / T$

Where $B_{2\beta}$ and $B_{2\beta}'$ are derivatives of B and C with respect to $\beta$. The derivative of A with respect to $\beta$ is zero.

Additionally basing on formula (23) we can derive sensitivity of the ramp response to $R_w$ directly and it appears that:

$$
S^R_{v_t} = S^\beta_{v_t}
$$

Hereunder it is shown the plot, which shows exemplary waveform of the ramp response sensitivity calculated from formula (23) for typical interconnect parameters of highly inductive interconnects. For comparison we present also the waveform from the PSPICE program. One can notice, that sensitivity of the ramp response obtained on the basis of the multiple scale method, which is approximate method, differ from the results obtained from PSPICE simulation (17).
Fig. 7 Comparison of sensitivity of ramp response with respect to $\beta$, as a function of time, obtained from formula (24) and from PSPICE, for exemplary parameters of highly inductive ($R_t=25\Omega$, $L_t=5nH$, $C_t=1pF$, $C_0=1pF$ and $R_w=25\Omega$, $\varepsilon=0.354$, $\alpha=10$, $\beta=0.354$), $a$ – sensitivity calculated in PSPICE, $b$ – sensitivity from formula (24).

Sensitivity analysis of the ramp response with respect to $C$ and $L$ parameters

The problem of sensitivity to changes of RLC parameters value connects with the exactness of extraction method of interconnects parameter. Analytical formulas for calculating per unit of
length parameters RLC of interconnect are still improved. The less the system is sensitive to small changes of parameters, the greater can be inaccuracy in performance of traces, but also in RLC parameter calculation. It is easy to show, that \( S^*_{\ell} = S_{\ell}^* \), therefore we concentrate only on sensitivity to C and L now. Sensitivity of ramp response to changes of per unit length capacitance C of interconnect using the definition of sensitivity, takes the form:

\[
S^C_{\ell}(t) = \frac{C}{v_{r}(t)} \frac{\partial v_{r}(t)}{\partial C}
\]

Using directly definition, it is necessary to remember, that normalized time is function of C also. It results from this, that calculation have to be done in the not normalized time domain as follow:

\[
S^C_{\ell}(\tilde{t}) = \left[ \frac{1}{C_0} v_{r}' + \frac{1}{2\sqrt{LC}} (R_{w} v_{r} + R v_{r}) - v_{r} \frac{\tilde{t}}{2C} + 1 \right] \frac{C}{v_{r}}
\]

\[
= S_{\ell}^C + \frac{1}{2} \left( S_{\ell}^C + S_{\ell}^C \right) - v_{r} \frac{\tilde{t}}{2} + 1
\]

Taking into account that:

\[
\tilde{t} = \left( \frac{t - T}{T} \right) = \frac{t}{T} - 1 = \frac{t}{\sqrt{L} \cdot C} - 1 \Rightarrow \tilde{t} = f(L, C)
\]

We have:

\[
\frac{d v_{r}}{d C} = \frac{\partial v_{r}}{\partial \alpha} \frac{\partial \alpha}{\partial C} + \frac{\partial v_{r}}{\partial \beta} \frac{\partial \beta}{\partial C} + \frac{\partial v_{r}}{\partial \xi} \frac{\partial \xi}{\partial C} + \frac{\partial v_{r}}{\partial \tilde{t}} \frac{\partial \tilde{t}}{\partial C}
\]

and after simple but tedious calculations we arrive to formula:

\[
S^C_{\ell}(t) = \frac{C}{v_{r}(t)} \frac{\partial v_{r}(t)}{\partial C}
\]

\[
= \frac{C}{v_{r}(t)} \frac{\partial}{\partial C} \left[ \frac{E_0}{\beta + 1} \left( A_2 \frac{t - \sqrt{LC}}{\sqrt{LC}} e^{-\frac{\alpha - \sqrt{LC}}{\sqrt{LC}}} - B_2 \left( 1 - e^{-\frac{\alpha - \sqrt{LC}}{\sqrt{LC}}} \right) + C_2 \frac{t - \sqrt{LC}}{\sqrt{LC}} \right) \right]
\]

On the following plots it is presented the dependence of sensitivity of the ramp response to the per unit length capacitance C. The plot on Fig 15 (Fig 15) presents comparison sensitivity obtained in PSPICE program and from formula (29). The sensitivity in the beginning phase of time-duration of signal is very large and when \( t \to T \) reaches the infinite value. In the case of using PSPICE simulations and formula (16) the error for time close to T is very large, therefore we take constant sensitivity equal to \( 1/\Delta \lambda \) for the time range from T to \( T = \sqrt{LC(1 + \Delta \lambda)} \).
Fig. 9 Sensitivity of ramp response to per unit length capacitance of interconnect, for exemplary parameters of highly inductive interconnects ($R_t=25\Omega$, $L_t=5nH$, $C_t=1pF$, $C_0=0.5pF$ and $R_w=25\Omega$, $\varepsilon=0.354$, $\alpha=2$, $\beta=0.354$) for various values of interconnect capacitance $C$. a – for $C=1pF$, b – for $C=0.5pF$, c – for $C=0.2pF$, d – for $C=0.1pF$.

Inductance of interconnect understood as an inductance per unit length of transmission line modeling interconnect is for top level interconnects very important factor, neglecting of which can cause large errors in simulation. Inductance significantly affects on signal integrity, hence influence of inductance on sensitivity of ramp response is very important element of top level layers of interconnects analysis. Sensitivity of ramp response to inductance, according to definition, can be written in the following form:

$$S_L = \frac{L \cdot \partial v_r}{v_r \cdot \partial L}$$  \hspace{1cm} (30)

Similarly as previously it is yet possible to exploit previously derived relationships on sensitivity to parameters $\alpha$, $\beta$, $\varepsilon$, for normalized time as:

$$\frac{d v_r}{d L} = \frac{\partial v_r}{\partial \alpha} \frac{\partial \alpha}{\partial L} + \frac{\partial v_r}{\partial \beta} \frac{\partial \beta}{\partial L} + \frac{\partial v_r}{\partial \varepsilon} \frac{\partial \varepsilon}{\partial L} + \frac{\partial v_r}{\partial \varepsilon} \frac{\partial \varepsilon}{\partial L}$$  \hspace{1cm} (31)

For not normalized time we obtain:

$$S_L' (\tilde{t}) = -\frac{1}{2} \left( S_\varepsilon' (\tilde{t}) + S_\varepsilon' (\tilde{t}) \right) - \frac{v_r'}{v_r} \frac{t}{2\sqrt{LC}}$$  \hspace{1cm} (32)

where $v_r'$ is a derivative of $v_r$ to normalized time $\tilde{t}$.

Sensitivity calculated analytically (32) and in PSPICE program are in good agreement. To calculate sensitivity in PSPICE program we use formula (17), therefore at the moment $t=T$ we will
designate it value $1/\Delta \lambda$. However in fact at $t=T$ it takes infinite value (Fig. 9). This improper value of sensitivity is designated in the time range $\sqrt{LC} < t < \sqrt{LC(1 + d\lambda)}$.

On the following plots we present sensitivity of ramp response to interconnect inductance for various parameters of inverter-interconnect-inverter system as: $R, L, C, R_{\text{w}}, C_{0}$.

Fig. 10 Sensitivity of ramp response to per unit length inductance of interconnect, for exemplary parameters of highly inductive ($R_t=25\Omega, L_t=5\text{nH}, C_t=1\text{pF}, C_0=0.5\text{pF}$ and $R_{\text{w}}=25\Omega, \leftrightarrow \varepsilon=0.354, \alpha=2, \beta=0.354$) for various values of interconnect capacitance $C$. a – for $C=1\text{pF}$, b – for $C=0.5\text{pF}$, c – for $C=0.2\text{pF}$, d – for $C=0.1\text{pF}$.

Analysis of ramp response, for which exemplary plots has been presented above, permits to formulate the conclusion, that sensitivity of ramp response to interconnect inductance for early times is very large, yet after time $2T$ (from the beginning of the time axis) is, in considered here range of parameters, less that one. Very large values of sensitivity for times close to $T$ are result of dependence of $T$ on inductance $L$ (for $t=T$ sensitivity achieves value infinity). As time is going down, time delay caused by change of inductance has no such meaning. In this connection it seems very important, form the point of view of threshold crossing time calculation, correct calculation of interconnect inductance.

**Sensitivity of threshold crossing time of ramp response**

Sensitivity of crossing time over voltage threshold to interconnect parameters is equivalent to relative change of the threshold crossing time to a change of parameter. In this section we present the sensitivity analysis of threshold crossing time to per unit length interconnect parameters $R, L, C$ and to parameters connected with load and supply also. Time of crossing over voltage threshold, what was shown above, can be determined on the basis of step or/and ramp responses for majority of cases of low loss and highly inductive interconnects in the time range $T_r<t<3T$. In this connection the threshold value we calculate from formula. Sensitivity of threshold crossing time to parameter $\lambda$ can be defined:

$$S_{t_{pr}}^1 = \frac{\partial t_{pr}}{\partial \lambda} \frac{\lambda}{t_{pr}}$$  \hspace{1cm} (33)

In general case, when it is essential to use iterative formula for threshold crossing time (15), calculation of sensitivity from formula directly is impossible. Thus it is necessary to create the
method, which enable sensitivity calculation on the basis of threshold crossing time relationship. The proposed method consist in differentiation of the both sides of equation:

\[ \rho(\beta + 1)\tau_r = K_1\tau e^{-\alpha\tau}T_2 + C_2\tau e^{-\alpha\tau}(K_2T_2 - K_1\tau e^{\alpha\tau}) - \frac{1}{2}C_2\tau^2 - B_2\tau_r \]  

(34)

and next calculation of sensitivity. Detailed formulas and derivations will be shown in the next sections.

**Sensitivity to interconnect model parameters**

We start our calculation of sensitivity with respect to following parameters: perturbation parameter \( \varepsilon \), load parameter \( \alpha \), supply parameter \( \beta \) and \( T_r \). Calculations of sensitivity to \( \varepsilon \) require differentiation of (34) with respect to \( \varepsilon \) taking into account, that \( \dot{t}_{pr} = (\dot{t}_{pr} - \sqrt{IC})/\sqrt{IC} \) depends on \( \varepsilon \). After differentiation (34) we obtain:

\[
0 = \frac{\partial K_1}{\partial \varepsilon} \dot{t}_{pr} e^{-\alpha t_{pr} T_2} + K_1 \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} e^{-\alpha t_{pr} T_2} + K_1 \dot{t}_{pr} e^{-\alpha t_{pr} (-\alpha) \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} T_2} + \frac{\partial C_2}{\partial \varepsilon} \dot{t}_{pr} + \frac{\partial C_2}{\partial \varepsilon} \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} + \frac{\partial B_2}{\partial \varepsilon} \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} T_r
\]

\[
+ \alpha e^{-\alpha t_{pr} \frac{\partial t_{pr}}{\partial \varepsilon}} (K_2T_2 - K_1T_r e^{\alpha T_r}) - e^{-\alpha t_{pr} \frac{\partial t_{pr}}{\partial \varepsilon}} \left( \frac{\partial K_2}{\partial \varepsilon} T_2 - \frac{\partial K_1}{\partial \varepsilon} \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} T_r e^{\alpha T_r} \right) - \frac{1}{2} \frac{\partial C_2}{\partial \varepsilon} \frac{\partial \dot{t}_{pr}}{\partial \varepsilon}
\]

Denoting \( \tau_{pr} = \frac{\partial t_{pr}}{\partial \varepsilon} \) and solving the above equation with respect to \( \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} \) we have:

\[
\tau_{pr} = \frac{e^{-\alpha t_{pr}} \left( \frac{\partial K_2}{\partial \varepsilon} T_2 - \frac{\partial K_1}{\partial \varepsilon} \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} T_r e^{\alpha T_r} - \frac{\partial K_1}{\partial \varepsilon} \dot{t}_{pr} T_2 \right) - \frac{\partial C_2}{\partial \varepsilon} \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} T_r e^{\alpha T_r} + \frac{1}{2} \frac{\partial C_2}{\partial \varepsilon} \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} T^2_r + \frac{\partial B_2}{\partial \varepsilon} \frac{\partial \dot{t}_{pr}}{\partial \varepsilon} T_r}{K_1 e^{-\alpha t_{pr} T_2} + K_1 \dot{t}_{pr} e^{-\alpha t_{pr} (-\alpha) T_2} + C_2 T_r + \alpha e^{-\alpha t_{pr} T_r} (K_2 T_2 - K_1 T_r e^{\alpha T_r})}
\]

(36)

where derivative \( \frac{\partial A}{\partial \varepsilon}, \frac{\partial B}{\partial \varepsilon}, \frac{\partial C}{\partial \varepsilon} \) could be easily obtained.

Sensitivity to \( \varepsilon \) will be described by formula:

\[
S_{t_{pr}}^{\varepsilon} = \frac{\varepsilon}{\tau_{pr}}
\]

(37)

It is easy to show, that sensitivity of ramp response to \( \varepsilon \) is equivalent to sensitivity to interconnect resistance \( R \). Finally we have:

\[
S_{t_{pr}}^{R} = \frac{1}{Z_0} \frac{R}{\dot{t}_{pr}} \tau_{pr} = \frac{\varepsilon}{\dot{t}_{pr}} \dot{t}_{pr} \tau_{pr} = S_{t_{pr}}^{\varepsilon}
\]

(38)

The following plots present relationships of sensitivity of 50% threshold crossing time to perturbation parameter \( \varepsilon \) (resistivity of interconnect) for exemplary interconnect parameters.
Sensitivity of 50% threshold crossing time to perturbation parameter $\varepsilon$ (37), as a function of interconnect inductance $L$, for various values of interconnect resistance. Sensitivity has been drawn for parameters: a - $R_t=12.5\,\Omega$, $C_t=1\,\text{pF}$, $C_0=0.1\,\text{pF}$ and $R_w=25\,\Omega$, b - $R_t=25\,\Omega$, $C_t=1\,\text{pF}$, $C_0=0.1\,\text{pF}$ and $R_w=25\,\Omega$, c - $R_t=50\,\Omega$, $C_t=1\,\text{pF}$, $C_0=0.1\,\text{pF}$ and $R_w=25\,\Omega$.

The above analysis shows, that sensitivity of 50% threshold crossing time to interconnect resistance depends, in very small degree, on load capacitance and rise time of the input ramp signal. The largest influence on sensitivity has interconnect inductance and load capacitance. Large values of interconnect capacitance in connection with small interconnect inductance cause the largest sensitivity to interconnect resistance. Larger sensitivity have more lossy interconnects, similar influence we can notice when interconnect is supplied from inverter with larger output resistance.

Sensitivity analysis for interconnect inductance changes, permits to come to the conclusion, that larger inductance the smaller sensitivity to interconnect losses determined by parameter $\varepsilon$.

Sensitivity of crossing time threshold of the ramp signal to parameter $\beta$ can be executed by differentiation of (34) with respect to $\beta$:

$$\rho \tau_r = K_1 \frac{\partial \tilde{t}_p}{\partial \beta} e^{-\alpha \tilde{t}_p T_2} + K_1 \tilde{t}_p e^{-\alpha \tilde{t}_p T_2} + \frac{\partial C_2}{\partial \beta} \tau_r \tilde{t}_p + C_2 \tau_r \frac{\partial \tilde{t}_p}{\partial \beta} + ae^{-\alpha \tilde{t}_p} \frac{\partial \tilde{t}_p}{\partial \beta} (K_2 T_2 - K_1 \tau_r e^{\alpha \tau_r}) - e^{-\alpha \tilde{t}_p} \left( \frac{\partial K_2}{\partial \beta} T_2 - 0 \right)$$

(39)

Derivative of crossing time over voltage threshold is determined by formula

$$\tilde{t}_p' = \frac{\rho \tau_r - \frac{\partial C_2}{\partial \beta} \tau_r \tilde{t}_p + e^{-\alpha \tilde{t}_p} \frac{\partial K_2}{\partial \beta} T_2 + \frac{\partial C_2}{\partial \beta} \tau_r^2 + \frac{\partial B_2}{\partial \beta} \tau_r}{K_1 e^{-\alpha \tilde{t}_p T_2} - K_1 \tilde{t}_p e^{-\alpha \tilde{t}_p T_2} + C_2 \tau_r + ae^{-\alpha \tilde{t}_p} (K_2 T_2 - K_1 \tau_r e^{\alpha \tau_r})}$$

(40)

where

$$\frac{\partial K_2}{\partial \beta} = \frac{\theta (B_2 \alpha + A_2^2)}{\alpha^2} = \frac{1}{\alpha} \frac{\partial B_2}{\partial \beta}$$

Knowing (40) we can express sensitivity to $\beta$ as follows:
Basing on sensitivity to $\beta$ we can derive, like previously, sensitivity to source resistance $R_w$.

After some transformations we arrive to relationship:

$$
S_{t_{pr}}^\beta = \frac{\beta}{t_{pr}} \frac{R_w}{t_{pr}}
$$

(41)

Obtained relationships permits for obtaining values of sensitivities to output resistance of sending inverter for concrete interconnect parameters. The results has been compared with results of simulation in PSPICE program. Exemplary values of parameters are presented on a plot in Fig. 12.

Fig. 12 Sensitivity of 50% threshold crossing time to parameter $R_w$ (42), as a function of interconnect inductance $L$ for 3 values of interconnect resistance. Sensitivity has been drawn for parameters: $a$ - $R_t=25\Omega$, $C_t=1pF$, $C_0=0.1pF$ and $R_w=12.5\Omega$, $b$ - $R_t=25\Omega$, $C_t=1pF$, $C_0=0.1pF$ and $R_w=25\Omega$, $c$ - $R_t=25\Omega$, $C_t=1pF$, $C_0=0.1pF$ and $R_w=50\Omega$.

Simulations performed in order to make conclusions on influence interconnects model parameters, in particular interconnect inductance, on sensitivity crossing time over given voltage threshold, confirm, that the larger interconnect inductance and smaller resistance the smaller sensitivity to output resistance of sending inverter. One can also to notice, that rise time of ramp signal and interconnect capacitance have not large influence, if parameters fulfill conditions of lowloss interconnect. On the plot we have shown exemplary values of sensitivity to output resistance of sending inverter for selected interconnect parameters, which illustrate range of changes and tendency of sensitivity to changes of parameter $R_w$.

Sensitivity crossing time over voltage threshold to load capacitance, will be calculated from sensitivity to $\alpha$ parameter:

$$
S_{t_{pr}}^\alpha = \frac{\alpha}{t_{pr}}
$$

(43)

$$
\dot{t}_{pr}R C_a = \frac{d}{dC_0} = -\frac{C}{C_0} \frac{\partial}{\partial \alpha} = -\frac{C}{C_0} \dot{t}_{pr\alpha}
$$
\[ S_{t_{pr}}^{c_0} = -\frac{C}{C_0} \frac{\dot{t}_{pr}}{t_{pr}} \frac{C_0}{C_0} = \frac{\alpha}{t_{pr}} \dot{t}_{pr} = \dot{S}_{t_{pr}}^{c_0} \]  

(44)

where \( S_{t_{pr}}^{c_0} \) can be determined by differentiation of equation (34) and next solving it for derivative of crossing time over voltage threshold with respect to \( \alpha \). After differentiation of (34) we have:

\[
0 = \frac{\partial K_1}{\partial \alpha} \dot{t}_{pr} e^{-\alpha t_{pr} T_2} + K_1 \frac{\partial \dot{t}_{pr}}{\partial \alpha} e^{-\alpha t_{pr} T_2} - \alpha K_1 \dot{t}_{pr}^2 e^{-\alpha t_{pr} T_2} - K_1 \dot{t}_{pr} e^{-\alpha t_{pr} T_2} \left( T_2 - \frac{\partial \dot{t}_{pr}}{\partial \alpha} T_2 \right) \\
+ K_1 \dot{t}_{pr} e^{-\alpha t_{pr} T_2} \frac{\partial T_2}{\partial \alpha} + C_2 \tau_r \frac{\partial \dot{t}_{pr}}{\partial \alpha} + e^{-\alpha t_{pr} T_2} \dot{t}_{pr} (K_2 T_2 - K_1 \tau_r e^{\alpha \tau_r}) \\
+ e^{-\alpha t_{pr} T_2} \frac{\partial \dot{t}_{pr}}{\partial \alpha} + \frac{\partial K_2}{\partial \alpha} e^{-\alpha t_{pr} T_2} + \frac{\partial K_2}{\partial \alpha} \tau_r e^{\alpha \tau_r} - K_1 \tau_r e^{\alpha \tau_r} - \frac{\partial B_2}{\partial \alpha} \tau_r,
\]

(45)

Derivative \( \dot{t}_{pr} \) with respect to \( \alpha \) is expressed by formula:

\[
\dot{t}_{pr}^{\alpha} = \frac{e^{-\alpha t_{pr}} \left( \frac{\partial K_3}{\partial \alpha} T_2 + K_2 \frac{\partial T_2}{\partial \alpha} \tau_r e^{\alpha \tau_r} - K_1 \tau_r e^{\alpha \tau_r} - \frac{\partial K_3}{\partial \alpha} \dot{t}_{pr} T_2 \right)}{e^{-\alpha t_{pr}} \left( \alpha (K_2 T_2 - K_1 \tau_r e^{\alpha \tau_r}) + K_1 T_2 (1 - \alpha K_1 \dot{t}_{pr}) \right)} + C_2 \dot{t}_{pr}^{\alpha},
\]

(46)

where

\[
\frac{\partial K_1}{\partial \alpha} = A_2 / \alpha^2 \\
\frac{\partial K_2}{\partial \alpha} = -\tau_r e^{\alpha \tau_r} \\
\frac{\partial B_2}{\partial \alpha} = \frac{-A_2}{\alpha^2} - \frac{2A_2}{\alpha^3} - \frac{2A_2}{\alpha^2}
\]

Sensitivity to load capacitance for considered range of interconnect parameters is the largest for large values of load capacitance and its maximal value is 0.5. Significant influence on increasing of sensitivity have also large values of resistance, and small values of capacitance and inductance of interconnect, and large values of source resistance. In Fig. 13 is shown exemplary results for sensitivity to load capacitance (receiving inverter input capacitance) calculation for selected parameters illustrating influence of values of these parameters on sensitivity.
Fig. 13 Sensitivity of 50% threshold crossing time to load capacitance $C_0$ (44), as a function of interconnect inductance $L$ for 3 values of interconnect resistance. Sensitivity has been drawn for parameters: a - $R_t=12.5\,\Omega$, $C_t=1\,\mu F$, $C_0=0.1\,\mu F$ and $R_w=25\,\Omega$, b - $R_t=25\,\Omega$, $C_t=1\,\mu F$, $C_0=0.1\,\mu F$ and $R_w=25\,\Omega$, c - $R_t=50\,\Omega$, $C_t=1\,\mu F$, $C_0=0.1\,\mu F$ and $R_w=25\,\Omega$.

Sensitivity of threshold crossing time to the last of parameters determining properties of input/output, that is sensitivity to rise-time of the ramp signal can be calculate on the basis derivative of threshold crossing time with respect to variable the rise-time. Differentiation of the both sides of (34) give us:

\[
\rho(\beta + 1) = K_1 \frac{\partial \hat{t}_{pr}}{\partial \tau_r} e^{-\alpha \hat{t}_{pr} T_2} + K_1 \hat{t}_{pr} \left(-\alpha - \frac{\partial T_2}{\partial \tau_r} + C_2 \hat{t}_{pr} + \frac{\alpha e^{-\alpha \hat{t}_{pr}} \frac{\partial \hat{t}_{pr}}{\partial \tau_r}}{K_2 \hat{t}_{pr} - K_1 \hat{t}_{r} e^{\alpha \tau_r} \alpha T_2 - e^{-\alpha \hat{t}_{pr}} \left( K_2 \frac{\partial T_2}{\partial \tau_r} - K_1 e^{\alpha \tau_r} - K_1 \hat{t}_{r} \alpha \right) \right) + \frac{1}{2} C_2 2 \tau_r - B_2
\]

Solving the above equation with respect to derivative, we obtain:

\[
\hat{t}_{pr} = \frac{\rho(\beta + 1) + C_2 (\hat{t}_{pr} - \hat{t}_{pr}) + B_2 + e^{-\alpha \hat{t}_{pr}} e^{\alpha \tau_r} (A_1 (\tau_r - \hat{t}_{pr}) - B_2)}{C_2 \tau_r + e^{-\alpha \hat{t}_{pr}} (B_2 T_2 - K_1 \hat{t}_{pr} e^{\alpha \tau_r} - \alpha K_1 \hat{t}_{r} e^{\alpha \tau_r})}
\]

and next sought relationship for sensitivity in the form:

\[
\hat{t}_{pr} = \frac{\tau_r}{\hat{t}_{pr} - \hat{t}_{pr} \tau_r \hat{t}_{pr}}
\]

Sensitivity to rise-time of input ramp signal (49) in small degree depend on model of inverter-interconnect-inverter system parameters, particularly to the changes of interconnect resistance $R$. 

\[
\text{(49)}
\]
and output resistance of sending inverter $R_w$. Along with growth of inductance sensitivity value diminishes, similarly for changes of interconnect capacitance $C$ and load capacitance $C_0$ (Fig. 14). The strongest dependence of sensitivity is for changes of the rise-time, and for changes of the rise-time equal 70% the time delay $T$ can reach value 0.6, while for 10% for the same interconnect sensitivity is equal to 0.15.

Fig. 14 Sensitivity of 50% threshold crossing time $t_{\text{tr}}$ to rise-time input ramp signal (49), as a function of interconnect inductance $L$ for 3 values of sending inverter output resistance. Sensitivity has been drawn for parameters: a - $R_w=25\,\Omega$, $C_t=1\,\mu\text{F}$, $C_0=0.1\,\mu\text{F}$ and $R_w=12.5\,\Omega$, b - $R_w=25\,\Omega$, $C_t=1\,\mu\text{F}$, $C_0=0.1\,\mu\text{F}$ and $R_w=25\,\Omega$, c - $R_w=25\,\Omega$, $C_t=1\,\mu\text{F}$, $C_0=0.1\,\mu\text{F}$ and $R_w=50\,\Omega$.

We can calculate sensitivity of threshold crossing time to interconnect inductance $L$:

$$
\frac{d\hat{t}_{\text{tr}}}{dL} = \frac{\partial \hat{t}_{\text{tr}}}{\partial \alpha} \frac{\partial \alpha}{dL} + \frac{\partial \hat{t}_{\text{tr}}}{\partial \beta} \frac{\partial \beta}{dL} + \frac{\partial \hat{t}_{\text{tr}}}{\partial \varepsilon} \frac{\partial \varepsilon}{dL} \tag{50}
$$

where:

$$
\frac{\partial \alpha}{dL} = 0, \quad \frac{\partial \varepsilon}{dL} = -\frac{1}{2} \int \frac{C}{L} \frac{\partial \beta}{dL} = -\frac{1}{2} \frac{R_w}{L} \sqrt{C}
$$

in turn $\frac{\partial \hat{t}_{\text{tr}}}{\partial \alpha}$, $\frac{\partial \hat{t}_{\text{tr}}}{\partial \beta}$, $\frac{\partial \hat{t}_{\text{tr}}}{\partial \varepsilon}$ can be easily calculated.

On the plot is presented comparison of the results obtained by the analytical approach determining sensitivity of threshold crossing time to interconnect inductance with simulation done in program PSPICE. The results of analysis of sensitivity for various parameter values permits to say, that the largest influence on sensitivity value have load capacitance. Since significance of interconnect inductance on behavior of inverter-interconnect-inverter system sensitivity is the main problem of this paper.

On the following plot we present sensitivity for various sets of interconnect parameter values to illustrate the influence of these values on sensitivity.
Fig. 15 Sensitivity of 50% threshold crossing time to interconnect inductance $L$, as a function of interconnect inductance $L$ for 3 values of load capacitance $C_0$. Sensitivity has been drawn for parameters: a - $R_t=25\Omega$, $C_t=1$ pF, $C_0=0.1$ pF and $R_w=25\Omega$, b - $R_t=25\Omega$, $C_t=1$ pF, $C_0=0.5$ pF and $R_w=25\Omega$, c - $R_t=25\Omega$, $C_t=1$ pF, $C_0=1$ pF and $R_w=25\Omega$.

CONCLUSIONS

Analysis of sensitivity values to various parameters permits for general conclusions formulation. Sensitivity to the most of parameters decreases as inductance increase and resistance decrease. The large load capacitance means larger sensitivity to the most parameters too. The derived relationships as sensitivity functions permits for fast calculation of sensitivities for specific interconnect parameters, without carry out arduous response simulations and next, basing on them, using incremental method of analysis to calculate sensitivity. The presented analytical method, which exactness was confirmed by simulations, has application in analysis of interconnect of higher layers VLSI circuits, assuming, that losses of interconnect are small and can be treated as a small perturbation parameter.

REFERENCES

